

Notice of References Cited	Application/Control No. 10/733,272	Applicant(s)/Patent Under Reexamination CHEN ET AL.	
	Examiner DuyVu n. Deo	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,972,734	10-1999	Carichner et al.	438/106
	B	US-6,582,616	06-2003	Kang et al.	216/13
	C	US-6,262,477	07-2001	Mahulikar et al.	257/698
	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Vol. 1, 1986, pg 407-408
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.